

CLAIMS

What is claimed is:

- 5 1. A liquid crystal display comprising:
 - a plurality of gate lines disposed parallel to each other;
 - a plurality of data lines disposed parallel to each other and perpendicularly to said plurality of gate lines;
 - 10 switching elements each disposed in the proximity of one of intersections between said plurality of gate lines and said plurality of data lines;
 - a plurality of pixel electrodes disposed over said gate lines and said data lines via an interlayer insulating film, wherein, when viewed from upside, that is, from the side of a gap between adjacent pixel electrodes toward the side of said gate line, said gap between adjacent pixel electrodes at least 15 partially overlaps with said gate line; and
 - a plurality of control electrodes each disposed under said gap between adjacent pixel electrodes and over said gate line, wherein, when viewed from upside through said gap, said 20 control electrode covers said gate line.
2. A liquid crystal display as set forth in claim 1, wherein, when viewed from upside, said control electrode at least 25 overlaps with said gap between adjacent pixel electrodes in width direction.
3. A liquid crystal display as set forth in claim 1, wherein, when viewed from upside, said control electrode at least 30 overlaps with an area in which said gate line and said gap

between adjacent pixel electrodes overlap.

4. A liquid crystal display as set forth in claim 1, wherein, when viewed from upside, said control electrode overlaps with said gate line and with said gap between adjacent pixel electrodes.
5. A liquid crystal display as set forth in claim 1, wherein said control electrode has the same potential voltage as that of a source electrode of said switching element.
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6. A liquid crystal display as set forth in claim 1, wherein said control electrode is formed in the same layer as that of a source electrode of said switching element.
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7. A liquid crystal display as set forth in claim 1, wherein said control electrode is formed integrally with a source electrode of said switching element.
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8. A liquid crystal display as set forth in claim 1, wherein said control electrode has a single layer structure comprising metal or ITO.
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9. A liquid crystal display as set forth in claim 1, wherein said control electrode has a multi-layer structure, each layer thereof comprising metal or ITO.
10. A liquid crystal display as set forth in claim 1, wherein said interlayer insulating film comprises an organic film.

11. A liquid crystal display as set forth in claim 1, wherein
said liquid crystal display has a COT structure.
12. A liquid crystal display as set forth in claim 1, wherein
5 said liquid crystal display is a reflection type liquid crystal
display.
13. A liquid crystal display as set forth in claim 1, wherein
said control electrode and a source electrode of said switching
10 element are coupled via an extended portion of said source
electrode, and a contact hole for coupling said control electrode
and said pixel electrode is provided on said extended portion.
14. A liquid crystal display as set forth in claim 1, wherein a
15 contact hole for coupling said control electrode and said pixel
electrode is provided on said control electrode.
15. An electronic equipment which includes said liquid crystal
display as set forth in claim 1, in a display portion of said
20 electronic equipment.
16. A liquid crystal display comprising:
an active matrix substrate;
an opposing substrate which is opposed to said active
25 matrix substrate; and
a liquid crystal layer interposed between said active
matrix substrate and said opposing substrate;
wherein said active matrix substrate comprises:
a plurality of gate lines which are disposed on an
30 insulating substrate and which are disposed parallel to each

other;

a plurality of data lines which are disposed on said plurality of gate lines via a gate insulating film and which are disposed parallel to each other and perpendicularly to said

5 plurality of gate lines;

switching elements each disposed in the proximity of one of intersections between said plurality of gate lines and said plurality of data lines;

10 a plurality of pixel electrodes disposed over said gate lines and said data lines via an interlayer insulating film, wherein, when viewed from upside, that is, from the side of a gap between adjacent pixel electrodes toward the side of said gate line, said gap between adjacent pixel electrodes at least partially overlaps with said gate line; and

15 a plurality of control electrodes each disposed under said gap between adjacent pixel electrodes and over said gate line, wherein, when viewed from upside, said control electrode covers said gate line.

20 17. A liquid crystal display as set forth in claim 16, wherein said control electrode is formed integrally with a source electrode of said switching element.

25 18. A liquid crystal display as set forth in claim 16, wherein said control electrode and a source electrode of said switching element are coupled via an extended portion of said source electrode, and a contact hole for coupling said control electrode and said pixel electrode is provided on said extended portion.

30 19. A liquid crystal display as set forth in claim 16, wherein a

contact hole for coupling said control electrode and said pixel electrode is provided on said control electrode.